

# **DATA SHEET**

Product Name Low T.C.R Thick Film Chip Resistors

Part Name LT Series

# Uniroyal Electronics Global Co., Ltd.

88#, Longteng Road, Economic & Technical Development Zone, Kunshan, Jiangsu, China

Tel	+86 512 5763 1411 / 22 /33
Email	marketing@uni-royal.cn
Manufacture Plant	Uniroyal Electronics Industry Co., Ltd.
	Aeon Technology Corporation
	Royal Electronic Factory (Thailand) Co., Ltd.
	Royal Technology (Thailand) Co., Ltd.





# 1. <u>Scope</u>

- 1.1 This datasheet is the characteristics of Low T.C.R Thick Film Chip Resistors manufactured by UNI-ROYAL.
- 1.2 Low T.C.R  $\pm$  50PPM/°C
- 1.3 Suitable for reflow& wave soldering
- 1.4 Application precision medical equipment, Auto industrial control system ,Communication equipment ,IPAD, Portable computer ,LED lamps, intelligent home appliances.

# 2. Part No. System

Part No. includes 14 codes shown as below:

2.1 1<sup>st</sup>~4<sup>th</sup> codes: Part name. E.g.: LT02  $\checkmark$  LT03  $\checkmark$  LT05  $\checkmark$  LT06

2.2 5<sup>th</sup>~6<sup>th</sup> codes: Power rating.

E.g.: W=Normal Size		"1~	"1~G" = "1~16"							
Wattage	1/32	3/4	1/2	1/3	1/4	1/8	1/10	1/16	1/20	1
Normal Size	WH	07	W2	W3	W4	W8	WA	WG	WM	1W

If power rating is equal or lower than 1 watt, 5<sup>th</sup> code would be "W" and 6<sup>th</sup> code would be a number or letter.

E.g.: WA=1/10W W4=1/4W

2.3  $7^{\text{th}}$  code: Tolerance. E.g.: D=±0.5% F=±1% G=±2% J=±5% K=±10%

2.4 8<sup>th</sup>~11<sup>th</sup> codes: Resistance Value.

2.4.1 If value belongs to standard value of E-24 series, the  $8^{th}$  code is zero,  $9^{th} \sim 10^{th}$  codes are the significant figures of resistance value, and the  $11^{th}$  code is the power of ten.

- 2.4.2 If value belongs to standard value of E-96 series, the  $8^{th} \sim 10^{th}$  codes are the significant figures of resistance value, and the  $11^{th}$  code is the power of ten.
- 2.4.311<sup>th</sup> codes listed as following:

 $0=10^{0}$   $1=10^{1}$   $2=10^{2}$   $3=10^{3}$   $4=10^{4}$   $5=10^{5}$   $6=10^{6}$   $J=10^{-1}$   $K=10^{-2}$   $L=10^{-3}$   $M=10^{-4}$ 

2.5  $12^{\text{th}} \sim 14^{\text{th}}$  codes.

2.5.1 12<sup>th</sup> code: Packaging Type. E.g.: C=Bulk T=Tape/Reel

2.5.2 13th code: Standard Packing Quantity.

4=4,000pcs 5=5,000pcs C=10,000pcs D=20,000pcs E=15,000pcs

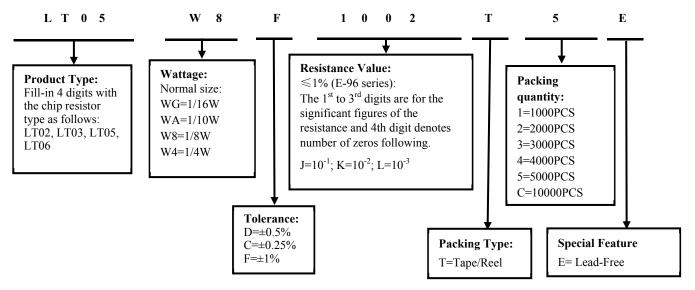
Chip Product: BD=B/B-20000pcs TC=T/R-10000pcs

2.5.3 14<sup>th</sup> code: Special features.

E = Environmental Protection, Lead Free, or Standard type.

#### 3. Ordering Procedure

#### (Example: LT05 1/8W ±1% 10KΩ T/R-5000)





333

2701

 $333 \rightarrow 33 \mathrm{K}\Omega$ 

 $2701 \rightarrow 2.7 \mathrm{K}\Omega$ 



### 4. Marking

# 4.1 $\pm$ 5% tolerance products (E-24 series):

3 codes.

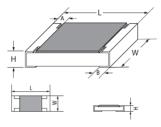
 $1^{\text{st}}\!\!\sim\!\!2^{nd}$  codes are the significant figures of resistance value, and the rest code is the power of ten.

4.2  $\pm$ 1% tolerance products (E-96 series):

4 codes.  $1^{st}\!\!\sim\!\!3^{rd}$  codes are the significant figures of resistance value, and the rest code is the power of ten. Letter "R" in mark means decimal point.

# 5. Dimension

T	Dimension(mm)									
Туре	L	W	Н	А	В					
LT02(0402)	1.00±0.10	0.50±0.05	0.35±0.05	0.20±0.10	0.25±0.10					
LT03(0603)	1.60±0.10	0.80±0.10	0.45±0.10	0.30±0.20	0.30±0.20					
LT05(0805)	2.00±0.15	1.25+0.15/-0.10	0.55±0.10	0.40±0.20	0.40±0.20					
LT06(1206)	3.10±0.15	1.55+0.15/-0.10	0.55±0.10	0.45±0.20	0.45±0.20					



# 6. <u>Resistance Range</u>

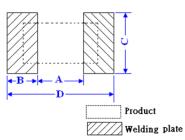
Туре	Power Rating	Resistance Range						
	at 70°C	0.25%	0.5%	1.0%				
LT02	1/16W	100Ω-1ΜΩ	100Ω-1ΜΩ	100Ω-1ΜΩ				
LT03	1/10W	1Ω-1ΜΩ	1Ω-1ΜΩ	1Ω-1ΜΩ				
LT05	1/8W	1Ω-1ΜΩ	1Ω-1ΜΩ	1Ω-1ΜΩ				
LT06	1/4W	1Ω-1ΜΩ	1Ω-1ΜΩ	1Ω-1ΜΩ				

# 7. Ratings:

Туре	Max. Working Voltage	Working Overload withstanding		<b>Operating</b> Temperature
LT02	50V	100V	100V	-55~+155℃
LT03	75V	150V	300V	-55℃~155℃
LT05	150V	300V	500V	-55℃~155℃
LT06	200V	400V	500V	-55℃~155℃

# 8. Soldering pad size recommended

Tuno	Dimension(mm)							
Туре –	Α	В	С	D				
LT02	$0.50 \pm 0.05$	$0.45 \pm 0.05$	$0.5 \pm 0.05$	$1.4{\pm}0.05$				
LT03	0.8±0.05	0.65±0.05	0.8±0.05	2.1±0.05				
LT05	1.0±0.1	1.0±0.1	1.3±0.1	3.0±0.1				
LT06	2.0±0.1	1.2±0.1	1.6±0.1	4.4±0.1				





Low T.C. R Thick Film Chip Resistors



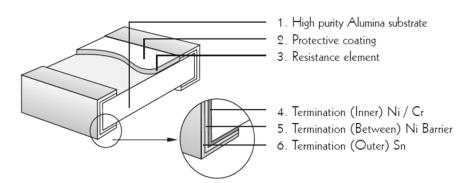
#### 9. Derating Curve:

Power rating will change based on continuous load at ambient temperature from -55 to 155°C. It is constant between -55 to 70°C, and derate to zero when temperature rise from 70 to 155°C. Voltage rating:

Resistors shall have a rated direct-current (DC) continuous working voltage or an approximate sine-wave root-mean-square (RMS) alternating-current (AC) continuous working voltage at commercial-line frequency and waveform corresponding to the power rating, as determined from the following formula:  $RCWV = \sqrt{P \times R}$ 

Remark: RCWV: Rating Continuous Working Voltage (Volt.) P: power rating (Watt) R: nominal resistance ( $\Omega$ ) In no case, the rated DC or RMS AC continuous working voltage must be greater than the applicable maximum value. The overload voltage is 2.5 times RCWV or Max. Overload voltage whichever is lower.

# 10. <u>Structure</u>



#### 11. Performance Specification

Characteristic	Limits	Test Methods (GB/T5729&JIS-C-5201&IEC60115-1)		
Temperature Coefficient	LT02: ±50ppm/°C LT03: 1Ω≤R≤10Ω: ±100ppm/°C 10Ω <r≤1mω: °c<br="" ±50ppm="">LT05: 1Ω≤R≤10Ω: ±100ppm/°C 10Ω<r≤1mω: °c<br="" ±50ppm="">LT06: 1Ω≤R≤10Ω: ±100ppm/°C 10Ω<r≤1mω: td="" °c<="" ±50ppm=""><td>4.8 Natural resistance changes per temp. Degree centigrade <math display="block">\frac{R_2-R_1}{R_1(t_2-t_1)} \times 10^6 (PPM/^{\circ}C)</math> R_1: Resistance Value at room temperature (t_1); R_2: Resistance at test temperature (Upper limit temperature or Lower limit temperature) t_1: +25^{\circ}C or specified room temperature t_2: Upper limit temperature or Lower limit temperature test temperature</td></r≤1mω:></r≤1mω:></r≤1mω:>	4.8 Natural resistance changes per temp. Degree centigrade $\frac{R_2-R_1}{R_1(t_2-t_1)} \times 10^6 (PPM/^{\circ}C)$ R_1: Resistance Value at room temperature (t_1); R_2: Resistance at test temperature (Upper limit temperature or Lower limit temperature) t_1: +25^{\circ}C or specified room temperature t_2: Upper limit temperature or Lower limit temperature test temperature		
Short-time overload	±(1.0%+0.05Ω)	4.13 Permanent resistance change after the application of a potential of 2.5 times RCWV or Max. Overload Voltage whiche less for 5 seconds		
Dielectric withstanding voltage	No evidence of flashover mechanical damage, arcing or insulation breaks down.	4.7 Resistors shall be clamped in the trough of a 90°C metallic v- block and shall be tested at ac potential respectively specified in the given list of each product type for 60-70 seconds.		
Solderability	95% coverage Min.	4.17 The area covered with a new, smooth, clean, shiny and continuous surface free from concentrated pinholes. Temperature of solder:245±3°C; Dwell time in solder: 2~3 seconds.		
Rapid change of temperature	±(1%+0.05Ω)	4.19 30 min at lower limit temperature and 30 min at upper limit temperature , 100 cycles.		
Soldering heat	±(1.0%+0.05Ω)	4.18 Dip the resistor into a solder bath having a temperature of 260 $^{\circ}C\pm5^{\circ}C$ and hold it for 10±1 seconds.		
Terminal bending	±(1.0%+0.05Ω)	4.33 Twist of test board: Y/X = 3/90 mm for 60Seconds		
Insulation resistance	≥1,000 MΩ	4.6 The measuring voltage shall be ,measured with a direct voltage of $(100\pm15)$ V or a voltage equal to the dielectric withstanding voltage., and apply for 1min.		



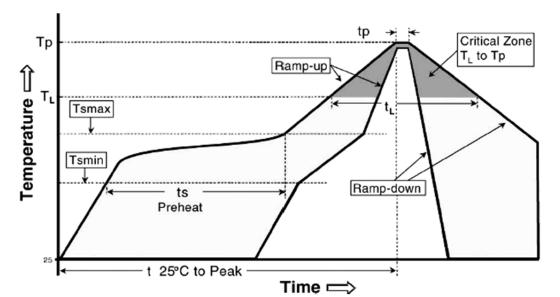


Humidity ( steady state )	±(0.5%+0.05Ω)	4.24Temporary resistance change after 240 hours exposure in a humidity test chamber controlled at $40\pm2$ °C and 90-95% relative humidity,				
Load life in humidity	±(1.0%+0.05Ω)	7.9 Resistance change after 1,000 hours (1.5 hours "ON",0.5 hour "OFF") at RCWV in a humidity chamber controlled at $40^{\circ}C\pm 2^{\circ}C$ and 90 to 95% relative humidity.				
Load life	±(1.0%+0.05Ω)	4.25.1 Permanent resistance change after 1,000 hours operating at RCWV with duty cycle 1.5 hours "ON", 0.5 hour "OFF" at $70^{\circ}C \pm 2^{\circ}C$ ambient.				
Low Temperature Storage	±(1.0%+0.05Ω)	4.23.4 Lower limit temperature , for 2H.				
High Temperature Exposure	±(1.0%+0.05Ω)	4.23.2 Upper limit temperature , for 1000H.				
Leaching	No visible damage	J-STD-002 Test D Samples completely immersed for 30 sec in solder bath at 260°C				

# 12. Soldering Condition

# (This is for recommendation, please customer perform adjustment according to actual application)

12.1 Recommend Reflow Soldering Profile : (solder : Sn96.5 / Ag3 / Cu0.5)



Profile Feature	Lead (Pb)-Free solder
Preheat:	
Temperature Min (Ts <sub>min</sub> )	150°C
Temperature Max (Ts <sub>max</sub> )	200°C
Time (Ts <sub>min</sub> to Ts <sub>max</sub> ) (ts)	60 -120 seconds
Average ramp-up rate:	
(Ts max to Tp)	$3^{\circ}$ C / second max.
Time maintained above :	
Temperature $(T_L)$	217°C
Time $(t_L)$	60-150 seconds
Peak Temperature (Tp)	260°C
Time within $^{+0}_{-5}$ °C of actual peak Temperature (tp) <sup>2</sup>	10 seconds
Ramp-own Rate	6°C/second max.
Time $25^{\circ}$ C to Peak Temperature	8mimutes max.

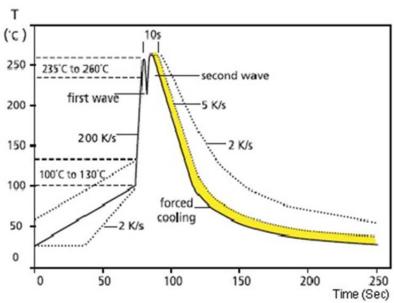
Allowed Re-flow times : 2 times

Remark : To avoid discoloration phenomena of chip on terminal electrodes, please use N2 Re-flow furnace .





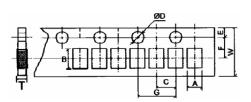
12.2 Recommend Wave Soldering Profile : (Apply to 0603 and above size)



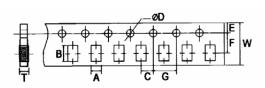
# 13. Packing of Surface Mount Resistors

13.1 Tapping Dimension : (Unit: mm)

Туре	A ±0.1	В ±0.1	С ±0.05	$\Phi D^{+0.1}_{-0}$	Е ±0.1	F ±0.05	G ±0.1	W ±0.2	T ±0.05
LT02	0.65	1.20	2.00	1.50	1.75	3.50	4.00	8.00	0.42

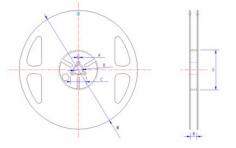


Tuno	А	В	С	$\Phi D_{-0}^{+0.1}$	Е	F	G	W	Т
Туре	±0.2	±0.2	$\pm 0.2$ $\pm 0.05$ $\Phi D_{-0}$	ΦD <sub>-0</sub>	±0.1	±0.05	±0.1	±0.2	±0.1
LT03	1.10	1.90	2.00	1.50	1.75	3.50	4.00	8.00	0.67
LT05	1.65	2.40	2.00	1.50	1.75	3.50	4.00	8.00	0.81
LT06	2.00	3.60	2.00	1.50	1.75	3.50	4.00	8.00	0.81



13.2 Dimension of Reel : (Unit: mm)

Туре	Taping	Qty/Reel	A±0.5	B±0.5	C±0.5	D±1	M±2	W±1
LT02	Paper	10,000pcs	2.0	13.0	21.0	60.0	178.0	10.0
LT03	Paper	5,000pcs	2.0	13.0	21.0	60.0	178.0	10.0
LT05	Paper	5,000pcs	2.0	13.0	21.0	60.0	178.0	10.0
LT06	Paper	5,000pcs	2.0	13.0	21.0	60.0	178.0	10.0







#### 14. Note

- 14.1. UNI-ROYAL recommend products store in warehouse with temperature between 15 to 35℃ under humidity between 25 to 75%RH. Even under storage conditions recommended above, solder ability of products will be degraded stored over 1 year old.
- 14.2. Cartons must be placed in correct direction which indicated on carton, otherwise the reel or wire will be deformed.
- 14.3. Storage conditions as below are inappropriate:
  - a. Stored in high electrostatic environment
  - b. Stored in direct sunshine, rain, snow or condensation.
  - c. Exposed to sea wind or corrosive gases, such as  $Cl_2$ ,  $H_2S$ ,  $NH_3$ ,  $SO_2$ ,  $NO_2$ , etc.

# 15. <u>Record</u>

Version	Description	Page	Date	Amended by	Checked by
1	First version	1~7	Mar.20, 2018	Haiyan Chen	Nana Chen
2	Modify characteristic	4~5	Feb.13, 2019	Haiyan Chen	Yuhua Xu
3	Modify the High Temperature Exposure conditions	7	July.29, 2019	Haiyan Chen	Yuhua Xu
4	Modify the reflow curve and add the wave soldering curve	5~6	Apr.22, 2020	Haiyan Chen	Yuhua Xu

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