

# **DATA SHEET**

**Product Name** Anti-Sulfurized Thick Film Chip Resistors Array-Convex Terminal

Part Name 2S02/4S02/4S03 Series

File No. SMD-SP-023

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#### 1. Scope

- 1.1 This datasheet is the characteristics of Anti-Sulfurized Thick Film Chip Resistors Array-Convex Terminal manufactured by UNI-ROYAL.
- 1.2 Anti-Sulfidation
- 1.3 Suitable for reflow & wave soldering
- 1.4 Application car, power
- 1.5 AEC-Q200 qualified
- 1.6 Compliant with RoHS directive.
- 1.7 Halogen free requirement.

#### 2. Part No. System

Part No. includes 14 codes shown as below:

- 2.1 1<sup>st</sup>~4<sup>th</sup> codes: Part name. E.g.: 2S02, 4S02, 4S03.
- 2.2 5<sup>th</sup>~6<sup>th</sup> codes: Power rating.

E.g.: W=Normal Size, WG=1/16W

- 2.3  $7^{th}$  code: Tolerance. E.g.:  $F=\pm 1\%$   $J=\pm 5\%$
- 2.4 8<sup>th</sup>~11<sup>th</sup> codes: Resistance Value.
- 2.4.1 If value belongs to standard value of E-24 series, the 8<sup>th</sup> code is zero, 9<sup>th</sup>~10<sup>th</sup> codes are the significant figures of resistance value, and the 11<sup>th</sup> code is the power of ten.
- 2.4.2 If value belongs to standard value of E-96 series, the  $8^{th} \sim 10^{th}$  codes are the significant figures of resistance value, and the  $11^{th}$  code is the power of ten.
- 2.4.311<sup>th</sup> codes listed as following:

 $0=10^{0}$   $1=10^{1}$   $2=10^{2}$   $3=10^{3}$   $4=10^{4}$   $5=10^{5}$   $6=10^{6}$   $J=10^{-1}$   $K=10^{-2}$   $L=10^{-3}$   $M=10^{-4}$ 

- 2.5 12<sup>th</sup>~14<sup>th</sup> codes.
- 2.5.1 12th code: Packaging Type. E.g.: T=Tape/Reel
- 2.5.2 13<sup>th</sup> code: Standard Packing Quantity.

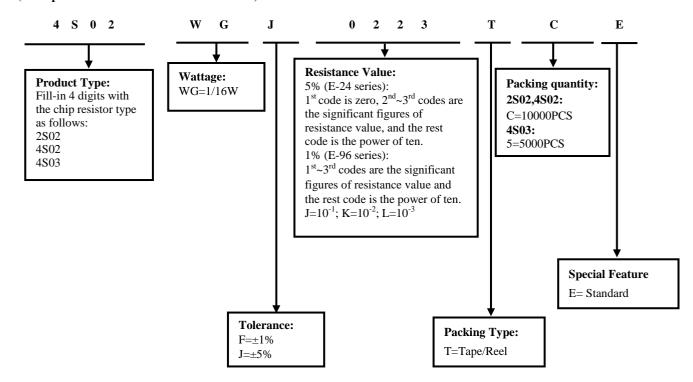
5=5,000pcs C=10,000pcs

2.5.3 14<sup>th</sup> code: Special features.

E= Standard

#### 3. Ordering Procedure

(Example:  $4S02\ 1/16W\ \pm 5\%\ 22K\Omega\ T/R-10000$ )



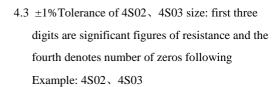






#### 4. Marking

- 4.1 Normal for 2S02 sizes, no marking on the body.0 $\Omega$  resistors is no marking too
- $4.2\pm\!5\%$  Tolerance of 4S02  $^\circ$  4S03 size: the first two digits are significant figures of resistance and the third denotes number of zeros following .



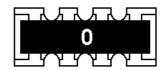
4.4. 0 $\Omega$ : Normal of 4S02 、 4S03size, the marking as following: Example: 4S02 、 4S03



 $333 \rightarrow 33K\Omega$ 



 $2701 \rightarrow 2.7 \text{K}\Omega$ 



## 5. Ratings & Dimension

Type	2S02	4S02	4S03		
Size	0402×2	0402×4	0603×4		
Dimension (mm)	1.0±0.1 0.15±0.05 0.17±0.1 0.25±0.1 0.25±0.1	9.05±0.05 9.07 9	0.30±0.15 0.50±0.15 0.50±0.15		
Equivalent Circuit Diagram	4 3 R1 R2 R1-R2	8 7 6 5 R1 R2 R3 R4 1 2 3 4 R1-R2-R3-R4	8 7 6 5 6 6 7 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7		

Туре	2S02	4S02	4S03
Power Rating at 70°C	1/16W	1/16W	1/16W
Resistance Value of jumper	<50mΩ	<50mΩ	<50mΩ
Jumper Rated Current	1A	1A	1A
Max Working Voltage	50V	50V	50V
Max Overload Voltage	100V	100V	100V
Dielectric Withstanding Voltage	100V	100V	300V
±1%	10Ω-1ΜΩ	10Ω-1ΜΩ	1Ω-1ΜΩ
±5%	10Ω-1ΜΩ	10Ω-1ΜΩ	1Ω-1ΜΩ
Operating Temperature		-55 ~ +155°C	

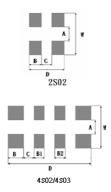






## 6. Soldering pad size recommended

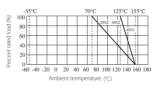
Т	Dimension(mm)								
Туре	A	В	B1	B2	W	С	D		
2S02	0.5±0.1	0.33±0.1	/	/	2.0±0.1	0.34±0.1	1.0±0.1		
4S02	0.5±0.1	0.3±0.1	0.28±0.1	0.28±0.1	2.0±0.1	0.22±0.1	1.82±0.1		
4S03	1.0±0.1	0.4±0.1	0.4±0.1	0.4±0.1	2.6±0.1	0.4±0.1	2.8±0.1		



#### 7. <u>Derating Curve</u>

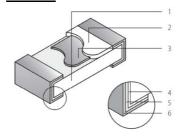
Power rating will change based on continuous load at ambient temperature from -55 to  $155^{\circ}$ C. It is constant between -55 to  $125^{\circ}$ C(2S02,4S02 is from -55 $^{\circ}$ C to  $70^{\circ}$ C), and derate to zero when Temperature rise from 125 to  $155^{\circ}$ C(2S02,4S02 is from  $70^{\circ}$ C to  $155^{\circ}$ C).

Resistors shall have a rated direct-current (DC) continuous working voltage or an approximate sine-wave root-mean-square (RMS) alternating-current (AC) continuous working voltage at commercial-line frequency and waveform corresponding to the power rating, as determined from the following formula:  $RCWV = \sqrt{P \times R}$ 



Remark: RCWV: Rating Continuous Working Voltage (Volt.) P: power rating (Watt) R: nominal resistance ( $\Omega$ ) In no case shall the rated DC or RMS AC continuous working voltage be greater than the applicable maximum value. The overload voltage is 2.5 times RCWV or Max. Overload voltage whichever is lower.

#### 8. Structure



- 1: High purity alumina substrate (96%AL<sub>2</sub>o<sub>3</sub> \ 0.3±0.1%CaO \ 1.0±0.3%MgO \ 2.1±0.05%SiO<sub>2</sub>)
- 2: Protective covering
- 3:Resistive covering ( Ag for  $0\Omega$  )
- 4: Termination (inner) Ag/Pd
- 5: Termination (between) Ni plating
- 6: Termination (outer) Sn plating

#### 9. Performance Specification

CI	Limits		D.C.C. 1 1	T (M (l. l.
Characteristic	Resistor	0Ω	Ref. Standards	Test Method
Operational life	±(3.0%+0.1Ω)	<100mΩ	MIL-STD-202 Method 108	<ul> <li>125°C, at 36% of operating power, 1000H (1.5 hours "ON" 0.5 hour "OFF"). Measurement at 24±4hours after test conclusion.</li> <li>Apply to rate current for 0 Ω</li> </ul>
Electrical Characterization (T.C.R)	2S02,4S02: $\pm 200 \text{ PPM/}^{\circ}$ C 4S03: $< 10 \Omega$ : $\pm 400 \text{ PPM/}^{\circ}$ C $≥ 10 \Omega$ : $\pm 200 \text{ PPM/}^{\circ}$ C	NA	GB/T 5729 4.8 JIS-C-5201 4.8 IEC 60115-1 6.2	Natural resistance changes per temp. Degree centigrade $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (PPM/°C)}$ $R_1(t_2 - t_1)$ $R_1: \text{ Resistance Value at room temperature }  (t_1) ;$ $R_2: \text{ Resistance at test temperature }  (t_2)$ $t_1: +25^{\circ}\text{C or specified room temperature}$ $t_2: \text{ Test temperature }  (-55^{\circ}\text{C or } 125^{\circ}\text{C})$
Short-time overload	±(2.0%+0.05Ω)	<50mΩ	GB/T 5729 4.13 JIS-C-5201 4.13 IEC 60115-1 8.1.4.2	Permanent resistance change after the application of a potential of 2.5 times RCWV or Max. Overload Voltage whichever less for 5 seconds Apply max Overload current for $0\Omega$







External Visual	Marking Complete, no mechanical	damage	MIL-STD-883 Method 2009	Electrical test not required.  Inspect device construction, marking and workmanship
Physical Dimension	Reference 5. Dimension Standards		JESD22 MH Method JB-100	Verify physical dimensions to the applicable device detail specification.  Note: User(s) and Suppliers spec. Electrical test not required.
Resistance to Solvent	Marking Complete , no mechanical	damage	MIL-STD-202 Method 215	Note: Add Aqueous wash chemical – OKEM Clean or equivalent. Do not use banned solvents.
Terminal Strength	Not broken		JIS-C-6429	Force of 1.8kg for 60 seconds.
High Temperature Exposure (Storage)	±(1%+0.1Ω)	<100mΩ	MIL-STD-202 Method 108	1000hrs. @T=155℃.Unpowered. Measurement at 24±4 hours after test conclusion.
Temperature Cycling	$\pm (1\% + 0.05\Omega)$	<100mΩ	JESD22 Method JA-104	1000 Cycles (-55 °C to +155 °C). Measurement at 24±4 hours after test conclusion.
Biased Humidity	±(3.0%+0.05Ω)	<100mΩ	MIL-STD-202 Method 103	1000 hours 85°C,85%RH.  Note: Specified conditions: 10% of operating power.  Measurement at 24±4 hours after test conclusion.
				Apply to rate current for $0 \Omega$
Vibration	$\pm (1\% \pm 0.1\Omega)$ <50m $\Omega$		MIL-STD-202 Method 204	5g's for 20 min., 12cycle each of 3 orientations.  Note: Use 8"*5"PCB. 031" thick 7 secure points onone long side and 2 secure points at corners of opposite sides. Parts mounted within 2' from any secure point. Test from 10-2000Hz.
Mechanical Shock	$\pm (1\%+0.1\Omega)$ <50m $\Omega$		MIL-STD-202 Method 213	Half sine wave, acceleration 100g's, each three times in X, Y and Z directions, pulse width 6ms.
ESD	±(3.0%+0.1Ω)	<50mΩ	AEC-Q200-002	With the electrometer in direct contact with the discharge tip, verify the voltage setting at levels of $\pm 500V, \pm 1KV, \pm 2KV, \pm 4KV, \pm 8KV$ , The electrometer reading shall be within $\pm 10\%$ for voltages from 500V to $\leq 800V$ .
Solderability	Coverage must be over 95%.		J-STD-020E	For both leaded & SMD. Electrical test not required.  Magnification 50X. Conditions:  a) Method B 4hrs at 155°C dry heat, the dip in bath with 245°C,5s.  b) b) Method D: at 260°C, 30±0.5s.
Flammability	No ignition of the tissue paper or so the pinewood board	corching or	UL-94	V-0 or V-1 are acceptable. Electrical test not required.
Board Flex	$\pm (1\% + 0.05\Omega)$	<50mΩ	AEC-Q200-005	Bending 2mm(min) for 60+5sec
Flame Retardance	No flame		AEC-Q200-001	Only requested, when voltage/power will increase the surface temp to 350°C. Apply voltage from 9V to 32V. No flame; No explosion.
Resistance to Soldering Heat	$\pm (1\% + 0.05\Omega)$ <50m $\Omega$		MIL-STD-202 Method 210	Condition B No per-heat of samples.  Dipping the resistor into a solder bath having a temperature of 260°C±5°C and hold it for 10±1 seconds
	$\pm (5.0\% + 0.05\Omega)$		/	Soaked in industrial oil with sulfur substance 3.5% contained 105°C±3°C, 500H
Sulfuration test	$\pm (1.0\% + 0.05\Omega)$		ASTMB-809- 95	Sulfur (Saturated vapor): Test temp.: 50°C Relative humidity: 86~90%RH Test time: 1000h



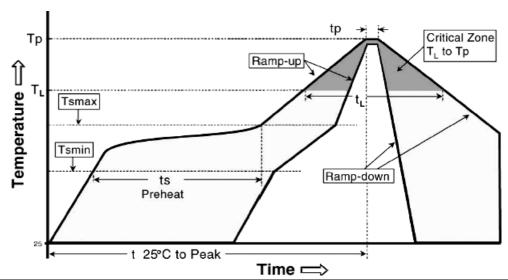




## 10. Soldering Condition

## (This is for recommendation, please customer perform adjustment according to actual application)

10.1 Recommend Reflow Soldering Profile: (solder: Sn96.5 / Ag3 / Cu0.5)

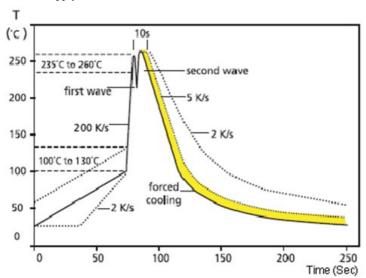


Profile Feature	Lead (Pb)-Free solder
Preheat:	
Temperature Min (Ts <sub>min</sub> )	150℃
Temperature Max (Ts <sub>max</sub> )	200℃
Time $(Ts_{min} \text{ to } Ts_{max})$ (ts)	60 -120 seconds
Average ramp-up rate:	
(Ts max to Tp)	$3^{\circ}\mathbb{C}$ / second max.
Time maintained above :	
Temperature $(T_L)$	217℃
Time (t <sub>L</sub> )	60-150 seconds
Peak Temperature (Tp)	260℃
Time within $^{+0}_{-5}$ °C of actual peak Temperature (tp) <sup>2</sup>	10 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8minutes max.

Allowed Re-flow times: 2 times

Remark : To avoid discoloration phenomena of chip on terminal electrodes, we suggest use  $N_2$  Re-flow furnace .

#### 10.2 Recommend Wave Soldering Profile: (Apply to 0603 and above size)





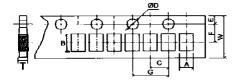




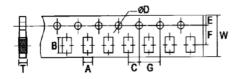
#### 11. Packing of Surface Mount Resistors

11.1 Dimension of Paper Taping:(Unit: mm)

Tumo	A	В	С	ФD+0.1	E	F	G	W	T
Type	±0.2	±0.2	±0.05	$\Phi D_{\_0}^{+0.1}$	±0.1	±0.05	±0.1	±0.2	±0.1
2S02	1.2	1.2	2.0	1.5	1.75	3.5	4.0	8.0	0.45
4S02	1.2	2.2	2.0	1.5	1.75	3.5	4.0	8.0	0.70

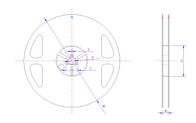


Т	A	В	C	ΦD+0.1	E	F	G	W	T
Type	±0.2	±0.2	±0.05	$\Phi D_{\_0}^{+0.1}$	±0.1	±0.05	±0.1	±0.2	±0.1
4S03	2.0	3.60	2.0	1.5	1.75	3.5	4.0	8.0	0.83



11.2 Dimension of Reel: (Unit: mm)

Туре	Qty/Reel	A±0.5	B±0.5	C±0.5	D±1.0	M±2.0	W±1.0
2S02	10,000PCS	2.0	13.0	21.0	60.0	178.0	10.0
4S02	10,000PCS	2.0	13.0	21.0	60.0	178.0	10.0
4S03	5,000PCS	2.0	13.0	21.0	60.0	178.0	10.0



#### 12. <u>Note</u>

- 12.1. UNI-ROYAL recommend products store in warehouse with temperature between 15 to 35 ℃ under humidity between 25 to 75%RH. Even under storage conditions recommended above, solder ability of products will be degraded stored over 1 year old.
- 12.2. Cartons must be placed in correct direction which indicated on carton, otherwise the reel or wire will be deformed.
- 12.3. Storage conditions as below are inappropriate:
  - a. Stored in high electrostatic environment
  - b. Stored in direct sunshine, rain, snow or condensation.
- 12.4 This product is used for automotive electronics. UNI-ROYAL will not be responsible for any damage, expense or loss caused by the use of this specification in any special environment. This series of products are suitable for automotive electronics applications, as shown below, If there are other applications, you need to confirm with UNI-ROYAL whether they are applicable:
  - a. Control unit for information, entertainment, navigation, audio;
  - b. Control unit for comfortable doors, windows, seat;
  - c. Control unit for internal lighting.

#### 13. Record

Version	Description	Page	Date	Amended by	Checked by
1	First version	1~7	Mar.20, 2018	Haiyan Chen	Nana Chen
2	Modify characteristic	4~5	Feb.18, 2019	Haiyan Chen	Yuhua Xu
3	1.Modify the reflow curve and add the wave soldering curve	6	Apr.30, 2020	Haiyan Chen	Yuhua Xu
	2. Notes for improvement				
4	Modify characteristic	4~5	Jun.26, 2023	Haiyan Chen	Yuhua Xu
5	Modify characteristic of ESD	5	Feb.19,2024	Song Nie	Haiyan Chen
6	1.Add AST-809-95 Sulfuration test 2.Modify the temperature coefficient	5 4	Feb.11,2025	Junying Ye	Haiyan Chen

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